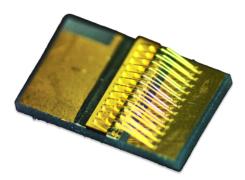
Preliminary Data Sheet





SemiNex delivers SOAs with the highest gain and available saturation power at infrared wavelengths. When necessary we will further optimize the design of our InP SOA to meet our customers' specific optical and electrical performance needs. Single waveguide or arrays are tested to meet customer and market performance demands. Typical results and packaging options are shown. Contact SemiNex for additional details or performance demands.



Semiconductor Optical Amplifier

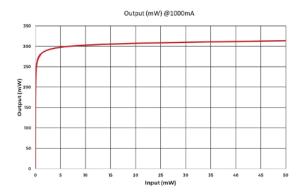
High Gain SemiNex SOA High Saturation Output Power Curved or Tilted Waveguide and Array 13xx and 15xx nm Custom Design and Waveguide available

- Applications

 FMCW LiDAR

 Telecom & Data Center
- Tunable Laser
- SpectroscopyResearch

- Features
 High Gain
 High Saturation Power
- High Efficiency
 Cost Effective



SOA Chips



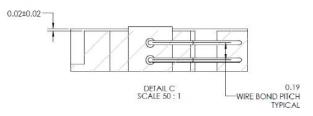


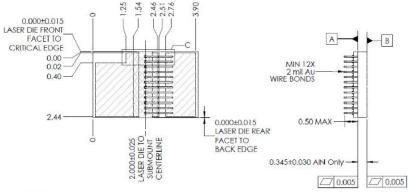
	Symbol	COC-290	Units
Optical			
Wavelength	λ_{c}	1310	nm (±20)
Output Power@1000mA	P _{out}	450	mW (±10%)
Aperture Width	AW	4	μm
Aperture Height	AH	1	μm
Spectral Width	δλ	85	nm @ 3dB
Gain @ Pin=10µW	G	40	dB
Beam Exit Angle	Θ _{EXT}	19.5	degree
Noise Figure	NF	6	db
Polarization Extinction Ratio	PER	18	dB
Fast Axis Div.	⊝_perp	30	deg FWHM
Slow Axis Div.	⊝_parallel	16	deg FWHM
Front Facet Reflectivity		<0.1%	
Rear Face Reflectivity		<0.1%	
Waveguide		Tilted Straight	
Electrical			
Operating Voltage	V _{op}	2	V
Operating Current	I _{op}	1	A
Mechanical			
Chip Length	CL	2500	μm
Chip Width	W	500	μm
Weight		0.05	g
Operating Temp.**		-40 to 100	°C
Storage Temp.		-40 to 100	°C

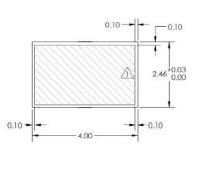
**Specified operating conditions are based on 20°C heat sink temperature. High temperature operation will reduce performance and MTTF.

**Specified values are based on the P-side down configuration and rated at a constant heat sink temperature of 20°C.

Unless otherwise indicated all values are nominal.







NOTES:

1) METALIZATION:

A-SIDE

☑ : Ti (0.06μm NOM) / Pt (0.2±0.04μm) / Aυ (0.6μm±0.12μm)

⊠ : Pt (0.32μm±0.064μm) / AuSn: Au 70±5wt% (3.0±0.6μm) B-SIDE

S : ΤΙ (0.06um NOM) / Pt (0.2±0.04μm) / Au (0.6μm±0.12μm)

2) EDGE QUALITY:

NO BURRS AND NO CHIPPING OF AREA CHIPPING OF DETAIL "C" < 40um OTHER EDGE CHIPPING < 50um

3) ARROW ON P-SIDE OF LASER DIE POINTS TOWARD THE FRONT FACET



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